

LSI LOGIC

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LSI Logic Corporation
Legal Department - IP
1621 Barber Lane, MS D-106
Milpitas, CA 95035Date September 27, 2005Number of pages including cover sheet 2

To: US PTO

Mail Stop Status Request

TC 2879

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From: **Connie del Castillo**

Intellectual Property Paralegal

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REMARKS:

☐ Urgent☐ For your review☒ Reply ASAP☐ Please comment

Application Number: 10/731,534
Filing date: December 8, 2003
First named inventor: William Voorhees et al.
Attorney docket number: 03-0055 / LSI.84US01

Transmitted herewith for filing via facsimile:

- Status Request Letter

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

William Voorhees et. al.

Serial No. :

10/731,534

Filed :

December 08, 2003

For :

Parallel Bus Debugging Tool

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§Group Art Unit :

2879

Examiner :Atty Docket :

LSI.84US01 / 03-0055

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SEP 27 2005


STATUS REQUESTCommissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

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Respectfully submitted,



Timothy Croll, Ph: [+1] 408-433-7191

Reg. No. 36,771

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